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| Notice of References Cited | Application/Control No. 10/044,670 | Applicant(s)/Patent Under Reexamination LEE ET AL. | |
| | Examiner Shelly A Chase | Art Unit 2133 | Page 1 of 2 |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|-----------------|----------------|
| | A | US-6,594,794 B1 | 07-2003 | De Marzi et al. | 714/784 |
| | B | US-6,256,762 B1 | 07-2001 | Beppu, Atsushi | 714/763 |
| | C | US-5,323,402 | 06-1994 | Vaccaro et al. | 714/782 |
| | D | US- | | | |
| | E | US- | | | |
| | F | US- | | | |
| | G | US- | | | |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
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| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|--|
| | U | Im et al., An advanced VLSI architecture of RS decoders for advanced TV, 1997, IEEE, pg. 1346-1350. |
| | V | Oh et al., AN efficient Reed-Solomon decoder VLSI with erasure correction, 1997, IEEE, pg. 193-201. |
| | W | Kwon et al., AN area-efficient VLSI architecture of a Reed-Solomon decoder/encoder for digital VCRs, 9-1997, IEEE, pg. 10191027. |
| | X | Chen et al., A new VLSI architecture of Reed Solomon decoder with erasure function, 1995, IEEE, pg. 1455-1459. |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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| | Examiner Shelly A Chase | Art Unit 2133 | Page 2 of 2 |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|------|----------------|
| | A | US- | | | |
| | B | US- | | | |
| | C | US- | | | |
| | D | US- | | | |
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| | F | US- | | | |
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| | M | US- | | | |

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|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
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| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|--|
| | U | Truong et al., Simplified procedure for correcting both errors and erasures of Reed-Solomon codes using Euclidean algorithm, 11-1998, IEE Proceedings, Vol. 135, Pt.E, No. 6, pg. 318-324. |
| | V | Burton, H, Inversionless Decoding of Binary BCH codes, 7-1971, IEEE, Trans. on Info. Theory, Vo. IT-17, No. 4, pg. 464-466 |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.